



# PIC16C62B/72A

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## PIC16C62B/72A Rev. B2 Silicon/Data Sheet Errata

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The PIC16C62B/72A Rev. B2 parts you have received conform functionally to the Device Data Sheet (DS35008B), except for the anomalies described below.

All the problems listed here will be addressed in future revisions of the PIC16C62B/72A silicon.

None.

# PIC16C62B/72A

## Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (DS35008B), the following clarifications and corrections should be noted.

### 1. Module: DC Characteristics (BOR Specifications)

The BOR specifications (parameters D001, D005 and the addition of parameter D005A), listed in Section 13.1 of the device data sheet, have been changed for Rev. B2 silicon. These new specifications apply to all devices with a date code of 0423xxx and later.

The following table shows the current test limits (modified values are shown in **bold**).

### 13.1 DC Characteristics: PIC16C62B/72A-04 (Commercial, Industrial, Extended) PIC16C62B/72A-20 (Commercial, Industrial, Extended)

DC CHARACTERISTICS		Standard Operating Conditions (unless otherwise stated) Operating temperature $0^{\circ}\text{C} \leq T_A \leq +70^{\circ}\text{C}$ for commercial $-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$ for industrial $-40^{\circ}\text{C} \leq T_A \leq +125^{\circ}\text{C}$ for extended					
Param No.	Sym	Characteristic	Min	Typ†	Max	Units	Conditions
D001	VDD	Supply Voltage	<b>VBOR</b>	—	<b>5.5</b>	<b>V</b>	<b>BOR enabled, FMAX ≤ 16 MHz</b>
D005	VBOR	Brown-out Reset voltage trip point	<b>3.65</b>	—	<b>4.35</b>	<b>V</b>	<b>BOR enabled, FMAX ≤ 16 MHz, TA ≥ -10°C</b>
<b>D005A</b>			<b>3.65</b>	—	<b>4.45</b>	<b>V</b>	<b>BOR enabled, FMAX ≤ 16 MHz, TA &lt; -10°C</b>

## REVISION HISTORY

Rev A Document (6/2004)

Original version of this document. Data Sheet Clarification  
issue 1 (DC Characteristics – BOR Specifications).

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NOTES:

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