

# SN5451, SN54LS51, SN54S51 SN7451, SN74LS51, SN74S51 AND-OR-INVERT GATES

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- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

## description

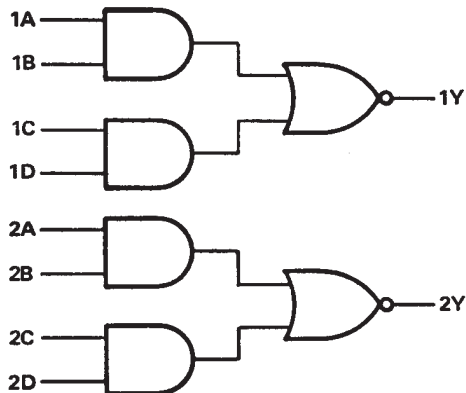
The '51 and 'S51 contain two independent 2-wide 2-input AND-OR-INVERT gates. They perform the Boolean function  $Y = \overline{AB + CD}$ .

The 'LS51 contains one 2-wide 3-input and one 2-wide 2-input AND-OR-INVERT gates. They perform the Boolean functions  $1Y = \overline{(1A \cdot 1B \cdot 1C) + (1D \cdot 1E \cdot 1F)}$  and  $2Y = \overline{(2A \cdot 2B) + (2C \cdot 2D)}$ .

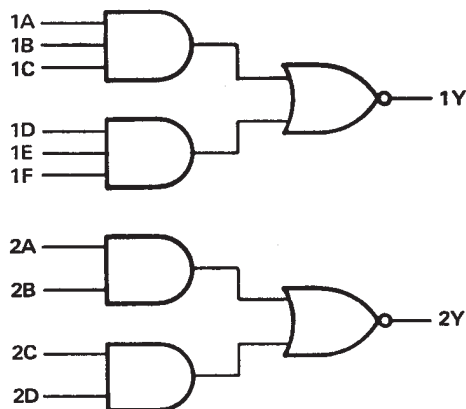
The SN5451, SN54LS51, and SN54S51 are characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN7451, SN74LS51 and SN74S51 are characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

## logic diagrams

'51, 'S51

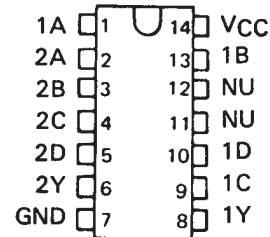


'LS51



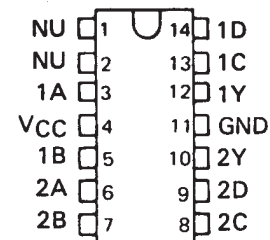
SN5451 . . . J PACKAGE  
SN54S51 . . . J OR W PACKAGE  
SN7451 . . . N PACKAGE  
SN74S51 . . . D OR N PACKAGE

(TOP VIEW)



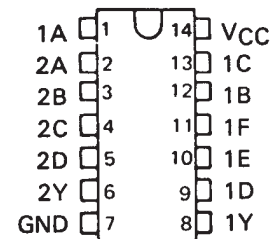
SN5451 . . . W PACKAGE

(TOP VIEW)



SN54LS51 . . . J OR W PACKAGE  
SN74LS51 . . . D OR N PACKAGE

(TOP VIEW)



NC - No internal connection

NU - Make no external connection

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

 **TEXAS  
INSTRUMENTS**

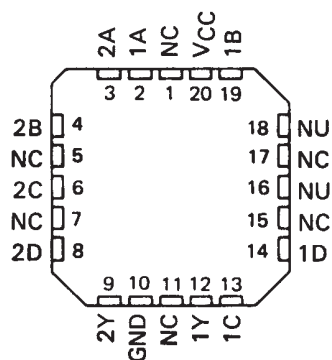
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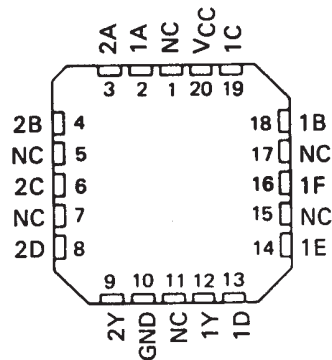
# SN5451, SN54LS51, SN54S51 SN7451, SN74LS51, SN74S51 AND-OR-INVERT GATES

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SN54S51 . . . FK PACKAGE  
(TOP VIEW)

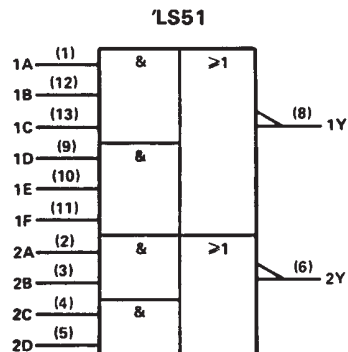
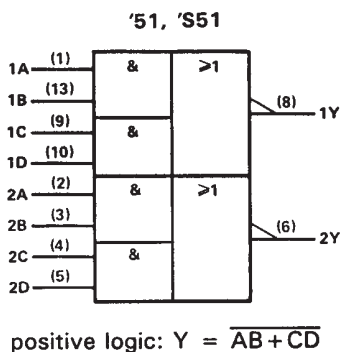


SN54LS51 . . . FK PACKAGE  
(TOP VIEW)



NC - No internal connection  
NU - Make no external connection

## logic symbols†



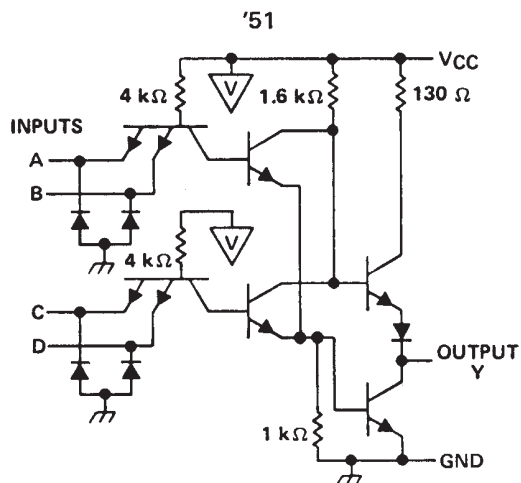
positive logic:

$$1Y = \overline{(1A \cdot 1B \cdot 1C) + (1D \cdot 1E \cdot 1F)}$$

$$2Y = \overline{(2A \cdot 2B) + (2C \cdot 2D)}$$

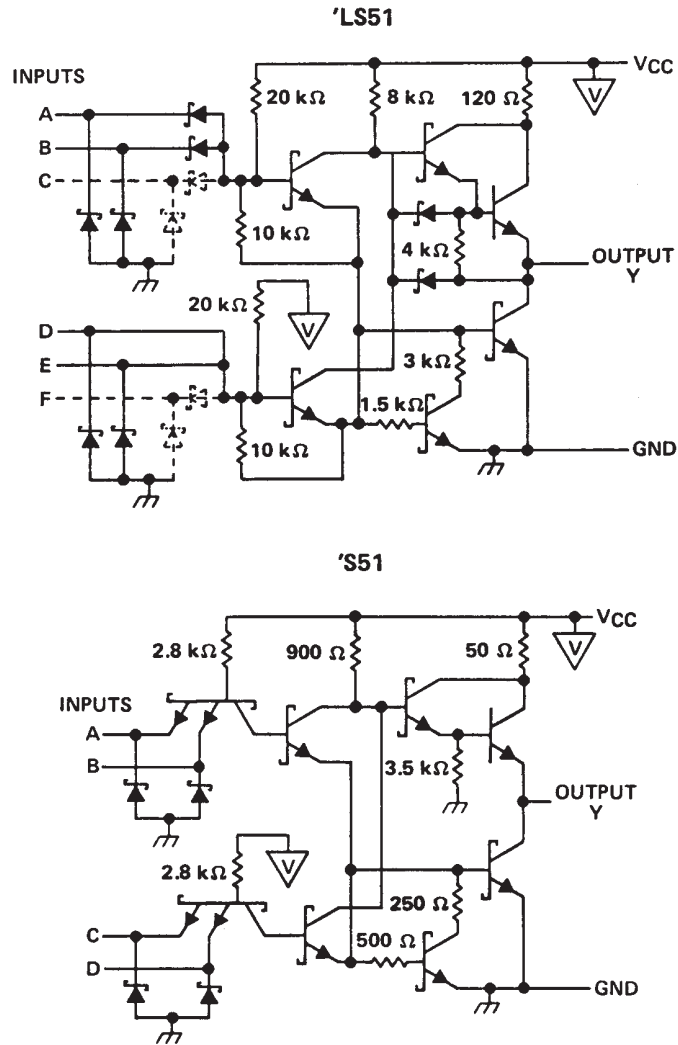
†These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.  
Pin numbers shown are for D, J, N, and W packages.

## schematics



SN5451, SN54LS51, SN54S51  
SN7451, SN74LS51, SN74S51  
AND-OR-INVERT GATES

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**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)**

|                                                               |                |
|---------------------------------------------------------------|----------------|
| Supply voltage, $V_{CC}$ (See Note 1): '51, 'LS51, 'S51 ..... | 7 V            |
| Input voltage: '51, 'S51 .....                                | 5.5 V          |
| 'LS51 .....                                                   | 7 V            |
| Operating free-air temperature range: SN54' .....             | -55°C to 125°C |
| SN74' .....                                                   | 0°C to 70°C    |
| Storage temperature range .....                               | -65°C to 150°C |

NOTE 1: Voltage values are with respect to network ground terminal.

# SN5451, SN54LS51, SN54S51 SN7451, SN74LS51, SN74S51 AND-OR-INVERT GATES

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## recommended operating conditions

|                                      | SN5451 |     |       | SN7451 |     |       | UNIT |
|--------------------------------------|--------|-----|-------|--------|-----|-------|------|
|                                      | MIN    | NOM | MAX   | MIN    | NOM | MAX   |      |
| $V_{CC}$ Supply voltage              | 4.5    | 5   | 5.5   | 4.75   | 5   | 5.25  | V    |
| $V_{IH}$ High-level input voltage    | 2      |     |       | 2      |     |       | V    |
| $V_{IL}$ Low-level input voltage     |        |     | 0.8   |        |     | 0.8   | V    |
| $I_{OH}$ High-level output current   |        |     | – 0.4 |        |     | – 0.4 | mA   |
| $I_{OL}$ Low-level output current    |        |     | 16    |        |     | 16    | mA   |
| $T_A$ Operating free-air temperature | – 55   |     | 125   | 0      |     | 70    | °C   |

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER  | TEST CONDITIONS †                                                       | SN5451 |       |       | SN7451 |       |       | UNIT |
|------------|-------------------------------------------------------------------------|--------|-------|-------|--------|-------|-------|------|
|            |                                                                         | MIN    | TYP ‡ | MAX   | MIN    | TYP ‡ | MAX   |      |
| $V_{IK}$   | $V_{CC} = \text{MIN}, I_I = -12 \text{ mA}$                             |        |       | – 1.5 |        |       | – 1.5 | V    |
| $V_{OH}$   | $V_{CC} = \text{MIN}, V_{IL} = 0.8 \text{ V}, I_{OH} = -0.4 \text{ mA}$ | 2.4    | 3.4   |       | 2.4    | 3.4   |       | V    |
| $V_{OL}$   | $V_{CC} = \text{MIN}, V_{IH} = 2 \text{ V}, I_{OL} = 16 \text{ mA}$     |        | 0.2   | 0.4   |        | 0.2   | 0.4   | V    |
| $I_I$      | $V_{CC} = \text{MAX}, V_I = 5.5 \text{ V}$                              |        |       | 1     |        |       | 1     | mA   |
| $I_{IH}$   | $V_{CC} = \text{MAX}, V_I = 2.4 \text{ V}$                              |        |       | 40    |        |       | 40    | µA   |
| $I_{IL}$   | $V_{CC} = \text{MAX}, V_I = 0.4 \text{ V}$                              |        |       | – 1.6 |        |       | – 1.6 | mA   |
| $I_{OS} §$ | $V_{CC} = \text{MAX}$                                                   | – 20   |       | – 55  | – 18   |       | – 55  | mA   |
| $I_{CCH}$  | $V_{CC} = \text{MAX}, V_I = 0 \text{ V}$                                |        | 4     | 8     |        | 4     | 8     | mA   |
| $I_{CCL}$  | $V_{CC} = \text{MAX}, \text{ See Note 2}$                               |        | 7.4   | 14    |        | 7.4   | 14    | mA   |

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at  $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$ .

§ Not more than one output should be shorted at a time.

NOTE 2: All inputs of one AND gate at 4.5 V, all others at GND.

## switching characteristics, $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$ (see note 3)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | TEST CONDITIONS                         | MIN | TYP | MAX | UNIT |
|-----------|--------------|-------------|-----------------------------------------|-----|-----|-----|------|
| $t_{PLH}$ | Any          | Y           | $R_L = 400 \Omega, C_L = 15 \text{ pF}$ |     | 13  | 22  | ns   |
| $t_{PHL}$ |              |             |                                         |     | 8   | 15  |      |

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

SN5451, SN54LS51, SN54S51  
SN7451, SN74LS51, SN74S51  
AND-OR-INVERT GATES

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recommended operating conditions

|                                      | SN54LS51 |     |       | SN74LS51 |     |       | UNIT |
|--------------------------------------|----------|-----|-------|----------|-----|-------|------|
|                                      | MIN      | NOM | MAX   | MIN      | NOM | MAX   |      |
| $V_{CC}$ Supply voltage              | 4.5      | 5   | 5.5   | 4.75     | 5   | 5.25  | V    |
| $V_{IH}$ High-level input voltage    | 2        |     |       | 2        |     |       | V    |
| $V_{IL}$ Low-level input voltage     |          |     | 0.7   |          |     | 0.8   | V    |
| $I_{OH}$ High-level output current   |          |     | – 0.4 |          |     | – 0.4 | mA   |
| $I_{OL}$ Low-level output current    |          |     | 4     |          |     | 8     | mA   |
| $T_A$ Operating free-air temperature | – 55     |     | 125   | 0        |     | 70    | °C   |

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER  | TEST CONDITIONS †                                                    | SN54LS51 |       |       | SN74LS51 |       |       | UNIT |
|------------|----------------------------------------------------------------------|----------|-------|-------|----------|-------|-------|------|
|            |                                                                      | MIN      | TYP ‡ | MAX   | MIN      | TYP ‡ | MAX   |      |
| $V_{IK}$   | $V_{CC} = \text{MIN}, I_I = -18 \text{ mA}$                          |          |       | – 1.5 |          |       | – 1.5 | V    |
| $V_{OH}$   | $V_{CC} = \text{MIN}, V_{IL} = \text{MAX}, I_{OH} = -0.4 \text{ mA}$ | 2.5      | 3.4   |       | 2.7      | 3.4   |       | V    |
| $V_{OL}$   | $V_{CC} = \text{MIN}, V_{IH} = 2 \text{ V}, I_{OL} = 4 \text{ mA}$   | 0.25     | 0.4   |       | 0.25     | 0.4   |       | V    |
|            | $V_{CC} = \text{MIN}, V_{IH} = 2 \text{ V}, I_{OL} = 8 \text{ mA}$   |          |       |       | 0.35     | 0.5   |       |      |
| $I_I$      | $V_{CC} = \text{MAX}, V_I = 7 \text{ V}$                             |          |       | 0.1   |          |       | 0.1   | mA   |
| $I_{IH}$   | $V_{CC} = \text{MAX}, V_I = 2.7 \text{ V}$                           |          |       | 20    |          |       | 20    | µA   |
| $I_{IL}$   | $V_{CC} = \text{MAX}, V_I = 0.4 \text{ V}$                           |          |       | – 0.4 |          |       | – 0.4 | mA   |
| $I_{OS} §$ | $V_{CC} = \text{MAX}$                                                | – 20     |       | – 100 | – 20     |       | – 100 | mA   |
| $I_{CCH}$  | $V_{CC} = \text{MAX}, V_I = 0 \text{ V}$                             |          | 0.8   | 1.6   |          | 0.8   | 1.6   | mA   |
| $I_{CCL}$  | $V_{CC} = \text{MAX}, \text{ See Note 2}$                            |          | 1.4   | 2.8   |          | 1.4   | 2.8   | mA   |

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at  $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$ .

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

NOTE 2: All inputs of one AND gate at 4.5 V, all others at GND.

switching characteristics,  $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$  (see note 3)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | TEST CONDITIONS                                | MIN | TYP  | MAX | UNIT |
|-----------|--------------|-------------|------------------------------------------------|-----|------|-----|------|
| $t_{PLH}$ | Any          | Y           | $R_L = 2 \text{ k}\Omega, C_L = 15 \text{ pF}$ |     | 12   | 20  | ns   |
| $t_{PHL}$ |              |             |                                                |     | 12.5 | 20  | ns   |

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



# SN5451, SN54LS51, SN54S51 SN7451, SN74LS51, SN74S51 AND-OR-INVERT GATES

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## recommended operating conditions

|                                      | SN54S51 |     |     | SN74S51 |     |      | UNIT |
|--------------------------------------|---------|-----|-----|---------|-----|------|------|
|                                      | MIN     | NOM | MAX | MIN     | NOM | MAX  |      |
| $V_{CC}$ Supply voltage              | 4.5     | 5   | 5.5 | 4.75    | 5   | 5.25 | V    |
| $V_{IH}$ High-level input voltage    | 2       |     |     | 2       |     |      | V    |
| $V_{IL}$ Low-level input voltage     |         |     | 0.8 |         |     | 0.8  | V    |
| $I_{OH}$ High-level output current   |         |     | – 1 |         |     | – 1  | mA   |
| $I_{OL}$ Low-level output current    |         |     | 20  |         |     | 20   | mA   |
| $T_A$ Operating free-air temperature | – 55    |     | 125 | 0       |     | 70   | °C   |

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER  | TEST CONDITIONS †                                                     | SN54S51 |       |       | SN74S51 |       |       | UNIT |
|------------|-----------------------------------------------------------------------|---------|-------|-------|---------|-------|-------|------|
|            |                                                                       | MIN     | TYP ‡ | MAX   | MIN     | TYP ‡ | MAX   |      |
| $V_{IK}$   | $V_{CC} = \text{MIN}, I_I = -18 \text{ mA}$                           |         |       | – 1.2 |         |       | – 1.2 | V    |
| $V_{OH}$   | $V_{CC} = \text{MIN}, V_{IL} = 0.8 \text{ V}, I_{OH} = -1 \text{ mA}$ | 2.5     | 3.4   |       | 2.7     | 3.4   |       | V    |
| $V_{OL}$   | $V_{CC} = \text{MIN}, V_{IH} = 2 \text{ V}, I_{OL} = 20 \text{ mA}$   |         |       | 0.5   |         |       | 0.5   | V    |
| $I_I$      | $V_{CC} = \text{MAX}, V_I = 5.5 \text{ V}$                            |         |       | 1     |         |       | 1     | mA   |
| $I_{IH}$   | $V_{CC} = \text{MAX}, V_I = 2.7 \text{ V}$                            |         |       | 50    |         |       | 50    | µA   |
| $I_{IL}$   | $V_{CC} = \text{MAX}, V_I = 0.5 \text{ V}$                            |         |       | – 2   |         |       | – 2   | mA   |
| $I_{OS} §$ | $V_{CC} = \text{MAX}$                                                 | – 40    |       | – 100 | – 40    |       | – 100 | mA   |
| $I_{CCH}$  | $V_{CC} = \text{MAX}, V_I = 0 \text{ V}$                              |         | 8.2   | 17.8  |         | 8.2   | 17.8  | mA   |
| $I_{CCL}$  | $V_{CC} = \text{MAX}, \text{ See Note 2}$                             |         | 13.6  | 22    |         | 13.6  | 22    | mA   |

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at  $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$ .

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

NOTE 2: All inputs of one AND gate at 4.5 V, all others at GND.

## switching characteristics, $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$ (see note 3)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | TEST CONDITIONS                         | MIN | TYP | MAX | UNIT |
|-----------|--------------|-------------|-----------------------------------------|-----|-----|-----|------|
| $t_{PLH}$ | Any          | Y           | $R_L = 280 \Omega, C_L = 15 \text{ pF}$ |     | 3.5 | 5.5 | ns   |
| $t_{PHL}$ |              |             |                                         |     | 3.5 | 5.5 | ns   |
| $t_{PLH}$ |              |             | $R_L = 280 \Omega, C_L = 50 \text{ pF}$ |     | 5   |     | ns   |
| $t_{PHL}$ |              |             |                                         |     | 5.5 |     | ns   |

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

**PACKAGING INFORMATION**

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup>    | Lead/<br>Ball Finish | MSL Peak Temp <sup>(3)</sup> | Samples<br>(Requires Login) |
|------------------|-----------------------|--------------|-----------------|------|-------------|----------------------------|----------------------|------------------------------|-----------------------------|
| JM38510/00502BCA | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| JM38510/07401BCA | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| JM38510/07401BDA | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| JM38510/30401B2A | ACTIVE                | LCCC         | FK              | 20   | 1           | TBD                        | POST-PLATE           | N / A for Pkg Type           |                             |
| JM38510/30401B2A | ACTIVE                | LCCC         | FK              | 20   | 1           | TBD                        | POST-PLATE           | N / A for Pkg Type           |                             |
| JM38510/30401BCA | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| JM38510/30401BCA | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| JM38510/30401BDA | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| JM38510/30401BDA | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| M38510/00502BCA  | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| M38510/00502BCA  | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| M38510/07401BCA  | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| M38510/07401BDA  | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| M38510/30401B2A  | ACTIVE                | LCCC         | FK              | 20   | 1           | TBD                        | POST-PLATE           | N / A for Pkg Type           |                             |
| M38510/30401B2A  | ACTIVE                | LCCC         | FK              | 20   | 1           | TBD                        | POST-PLATE           | N / A for Pkg Type           |                             |
| M38510/30401BCA  | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| M38510/30401BCA  | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| M38510/30401BDA  | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| M38510/30401BDA  | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SN5451J          | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SN5451J          | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SN54LS51J        | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SN54LS51J        | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SN54S51J         | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SN7451N          | OBSOLETE              | PDIP         | N               | 14   |             | TBD                        | Call TI              | Call TI                      |                             |
| SN7451N          | OBSOLETE              | PDIP         | N               | 14   |             | TBD                        | Call TI              | Call TI                      |                             |
| SN7451N3         | OBSOLETE              | PDIP         | N               | 14   |             | TBD                        | Call TI              | Call TI                      |                             |
| SN7451N3         | OBSOLETE              | PDIP         | N               | 14   |             | TBD                        | Call TI              | Call TI                      |                             |
| SN74LS51D        | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup>    | Lead/<br>Ball Finish | MSL Peak Temp <sup>(3)</sup> | Samples<br>(Requires Login) |
|------------------|-----------------------|--------------|-----------------|------|-------------|----------------------------|----------------------|------------------------------|-----------------------------|
| SN74LS51D        | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DE4      | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DE4      | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DG4      | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DG4      | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DR       | ACTIVE                | SOIC         | D               | 14   | 2500        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DR       | ACTIVE                | SOIC         | D               | 14   | 2500        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DRE4     | ACTIVE                | SOIC         | D               | 14   | 2500        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DRE4     | ACTIVE                | SOIC         | D               | 14   | 2500        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DRG4     | ACTIVE                | SOIC         | D               | 14   | 2500        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51DRG4     | ACTIVE                | SOIC         | D               | 14   | 2500        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51N        | ACTIVE                | PDIP         | N               | 14   | 25          | Pb-Free (RoHS)             | CU NIPDAU            | N / A for Pkg Type           |                             |
| SN74LS51N        | ACTIVE                | PDIP         | N               | 14   | 25          | Pb-Free (RoHS)             | CU NIPDAU            | N / A for Pkg Type           |                             |
| SN74LS51NE4      | ACTIVE                | PDIP         | N               | 14   | 25          | Pb-Free (RoHS)             | CU NIPDAU            | N / A for Pkg Type           |                             |
| SN74LS51NE4      | ACTIVE                | PDIP         | N               | 14   | 25          | Pb-Free (RoHS)             | CU NIPDAU            | N / A for Pkg Type           |                             |
| SN74LS51NSR      | ACTIVE                | SO           | NS              | 14   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51NSR      | ACTIVE                | SO           | NS              | 14   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51NSRE4    | ACTIVE                | SO           | NS              | 14   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51NSRE4    | ACTIVE                | SO           | NS              | 14   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup>    | Lead/<br>Ball Finish | MSL Peak Temp <sup>(3)</sup> | Samples<br>(Requires Login) |
|------------------|-----------------------|--------------|-----------------|------|-------------|----------------------------|----------------------|------------------------------|-----------------------------|
| SN74LS51NSRG4    | ACTIVE                | SO           | NS              | 14   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74LS51NSRG4    | ACTIVE                | SO           | NS              | 14   | 2000        | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74S51D         | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74S51D         | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74S51DE4       | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74S51DE4       | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74S51DG4       | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74S51DG4       | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS<br>& no Sb/Br) | CU NIPDAU            | Level-1-260C-UNLIM           |                             |
| SN74S51J         | OBSOLETE              | CDIP         | J               | 14   |             | TBD                        | Call TI              | Call TI                      |                             |
| SN74S51J         | OBSOLETE              | CDIP         | J               | 14   |             | TBD                        | Call TI              | Call TI                      |                             |
| SN74S51N         | ACTIVE                | PDIP         | N               | 14   | 25          | Pb-Free (RoHS)             | CU NIPDAU            | N / A for Pkg Type           |                             |
| SN74S51N         | ACTIVE                | PDIP         | N               | 14   | 25          | Pb-Free (RoHS)             | CU NIPDAU            | N / A for Pkg Type           |                             |
| SN74S51N3        | OBSOLETE              | PDIP         | N               | 14   |             | TBD                        | Call TI              | Call TI                      |                             |
| SN74S51N3        | OBSOLETE              | PDIP         | N               | 14   |             | TBD                        | Call TI              | Call TI                      |                             |
| SN74S51NE4       | ACTIVE                | PDIP         | N               | 14   | 25          | Pb-Free (RoHS)             | CU NIPDAU            | N / A for Pkg Type           |                             |
| SN74S51NE4       | ACTIVE                | PDIP         | N               | 14   | 25          | Pb-Free (RoHS)             | CU NIPDAU            | N / A for Pkg Type           |                             |
| SNJ5451J         | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SNJ5451J         | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SNJ5451W         | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SNJ5451W         | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SNJ54LS51FK      | ACTIVE                | LCCC         | FK              | 20   | 1           | TBD                        | POST-PLATE           | N / A for Pkg Type           |                             |
| SNJ54LS51FK      | ACTIVE                | LCCC         | FK              | 20   | 1           | TBD                        | POST-PLATE           | N / A for Pkg Type           |                             |
| SNJ54LS51J       | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SNJ54LS51J       | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |
| SNJ54LS51W       | ACTIVE                | CFP          | W               | 14   | 1           | TBD                        | A42                  | N / A for Pkg Type           |                             |

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup> | Lead/<br>Ball Finish | MSL Peak Temp <sup>(3)</sup> | Samples<br>(Requires Login) |
|------------------|-----------------------|--------------|-----------------|------|-------------|-------------------------|----------------------|------------------------------|-----------------------------|
| SNJ54LS51W       | ACTIVE                | CFP          | W               | 14   | 1           | TBD                     | A42                  | N / A for Pkg Type           |                             |
| SNJ54S51FK       | ACTIVE                | LCCC         | FK              | 20   | 1           | TBD                     | POST-PLATE           | N / A for Pkg Type           |                             |
| SNJ54S51J        | ACTIVE                | CDIP         | J               | 14   | 1           | TBD                     | A42                  | N / A for Pkg Type           |                             |
| SNJ54S51W        | ACTIVE                | CFP          | W               | 14   | 1           | TBD                     | A42                  | N / A for Pkg Type           |                             |

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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#### OTHER QUALIFIED VERSIONS OF SN5451, SN54LS51, SN54S51, SN7451, SN74LS51, SN74S51 :

• Catalog: [SN7451](#), [SN74LS51](#), [SN74S51](#)

• Military: [SN5451](#), [SN54LS51](#), [SN54S51](#)

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product
- Military - QML certified for Military and Defense Applications

**TAPE AND REEL INFORMATION**
**REEL DIMENSIONS**

**TAPE DIMENSIONS**

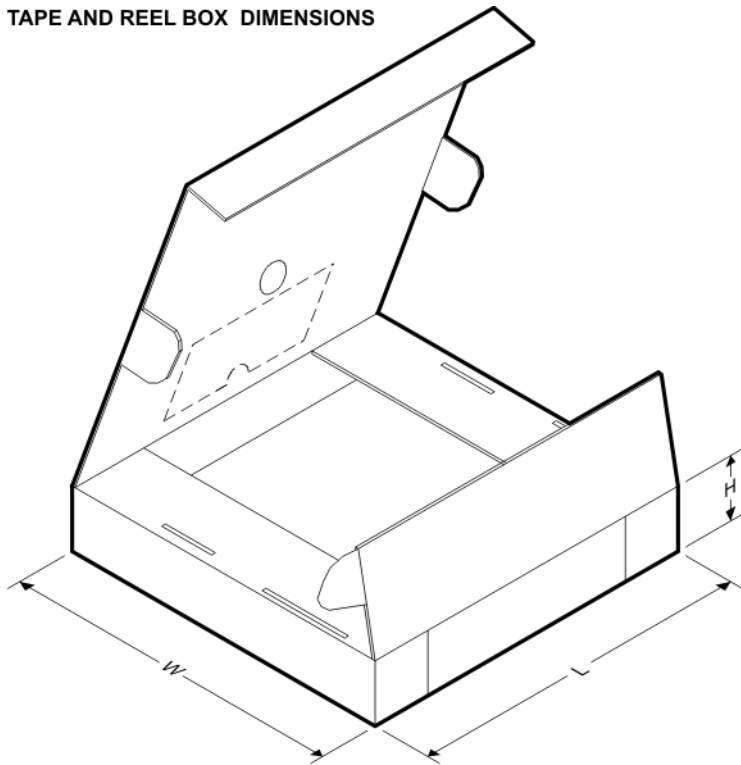

|    |                                                           |
|----|-----------------------------------------------------------|
| A0 | Dimension designed to accommodate the component width     |
| B0 | Dimension designed to accommodate the component length    |
| K0 | Dimension designed to accommodate the component thickness |
| W  | Overall width of the carrier tape                         |
| P1 | Pitch between successive cavity centers                   |

**TAPE AND REEL INFORMATION**

\*All dimensions are nominal

| Device      | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74LS51DR  | SOIC         | D               | 14   | 2500 | 330.0              | 16.4               | 6.5     | 9.0     | 2.1     | 8.0     | 16.0   | Q1            |
| SN74LS51NSR | SO           | NS              | 14   | 2000 | 330.0              | 16.4               | 8.2     | 10.5    | 2.5     | 12.0    | 16.0   | Q1            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

| Device      | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|-------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN74LS51DR  | SOIC         | D               | 14   | 2500 | 367.0       | 367.0      | 38.0        |
| SN74LS51NSR | SO           | NS              | 14   | 2000 | 367.0       | 367.0      | 38.0        |

J (R-GDIP-T\*\*)

14 LEADS SHOWN

# CERAMIC DUAL IN-LINE PACKAGE

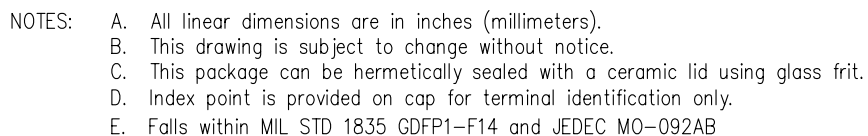


| PINS **<br>DIM | 14                     | 16                     | 18                     | 20                     |
|----------------|------------------------|------------------------|------------------------|------------------------|
| A              | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC |
| B MAX          | 0.785<br>(19,94)       | .840<br>(21,34)        | 0.960<br>(24,38)       | 1.060<br>(26,92)       |
| B MIN          | —                      | —                      | —                      | —                      |
| C MAX          | 0.300<br>(7,62)        | 0.300<br>(7,62)        | 0.310<br>(7,87)        | 0.300<br>(7,62)        |
| C MIN          | 0.245<br>(6,22)        | 0.245<br>(6,22)        | 0.220<br>(5,59)        | 0.245<br>(6,22)        |



4040083/F 03/03

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package is hermetically sealed with a ceramic lid using glass frit.
  - D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
  - E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.



FK (S-CQCC-N\*\*)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



| NO. OF<br>TERMINALS<br>** | A                |                  | B                |                  |
|---------------------------|------------------|------------------|------------------|------------------|
|                           | MIN              | MAX              | MIN              | MAX              |
| 20                        | 0.342<br>(8,69)  | 0.358<br>(9,09)  | 0.307<br>(7,80)  | 0.358<br>(9,09)  |
| 28                        | 0.442<br>(11,23) | 0.458<br>(11,63) | 0.406<br>(10,31) | 0.458<br>(11,63) |
| 44                        | 0.640<br>(16,26) | 0.660<br>(16,76) | 0.495<br>(12,58) | 0.560<br>(14,22) |
| 52                        | 0.740<br>(18,78) | 0.761<br>(19,32) | 0.495<br>(12,58) | 0.560<br>(14,22) |
| 68                        | 0.938<br>(23,83) | 0.962<br>(24,43) | 0.850<br>(21,6)  | 0.858<br>(21,8)  |
| 84                        | 1.141<br>(28,99) | 1.165<br>(29,59) | 1.047<br>(26,6)  | 1.063<br>(27,0)  |



4040140/D 01/11

- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package can be hermetically sealed with a metal lid.
  - Falls within JEDEC MS-004

N (R-PDIP-T\*\*)

16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



| PINS **<br>DIM      | 14               | 16               | 18               | 20               |
|---------------------|------------------|------------------|------------------|------------------|
| A MAX               | 0.775<br>(19,69) | 0.775<br>(19,69) | 0.920<br>(23,37) | 1.060<br>(26,92) |
| A MIN               | 0.745<br>(18,92) | 0.745<br>(18,92) | 0.850<br>(21,59) | 0.940<br>(23,88) |
| MS-001<br>VARIATION | AA               | BB               | AC               | AD               |



4040049/E 12/2002

NOTES:

- A. All linear dimensions are in inches (millimeters).  
B. This drawing is subject to change without notice.
-  Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).  
 The 20 pin end lead shoulder width is a vendor option, either half or full width.

D (R-PDSO-G14)

PLASTIC SMALL OUTLINE

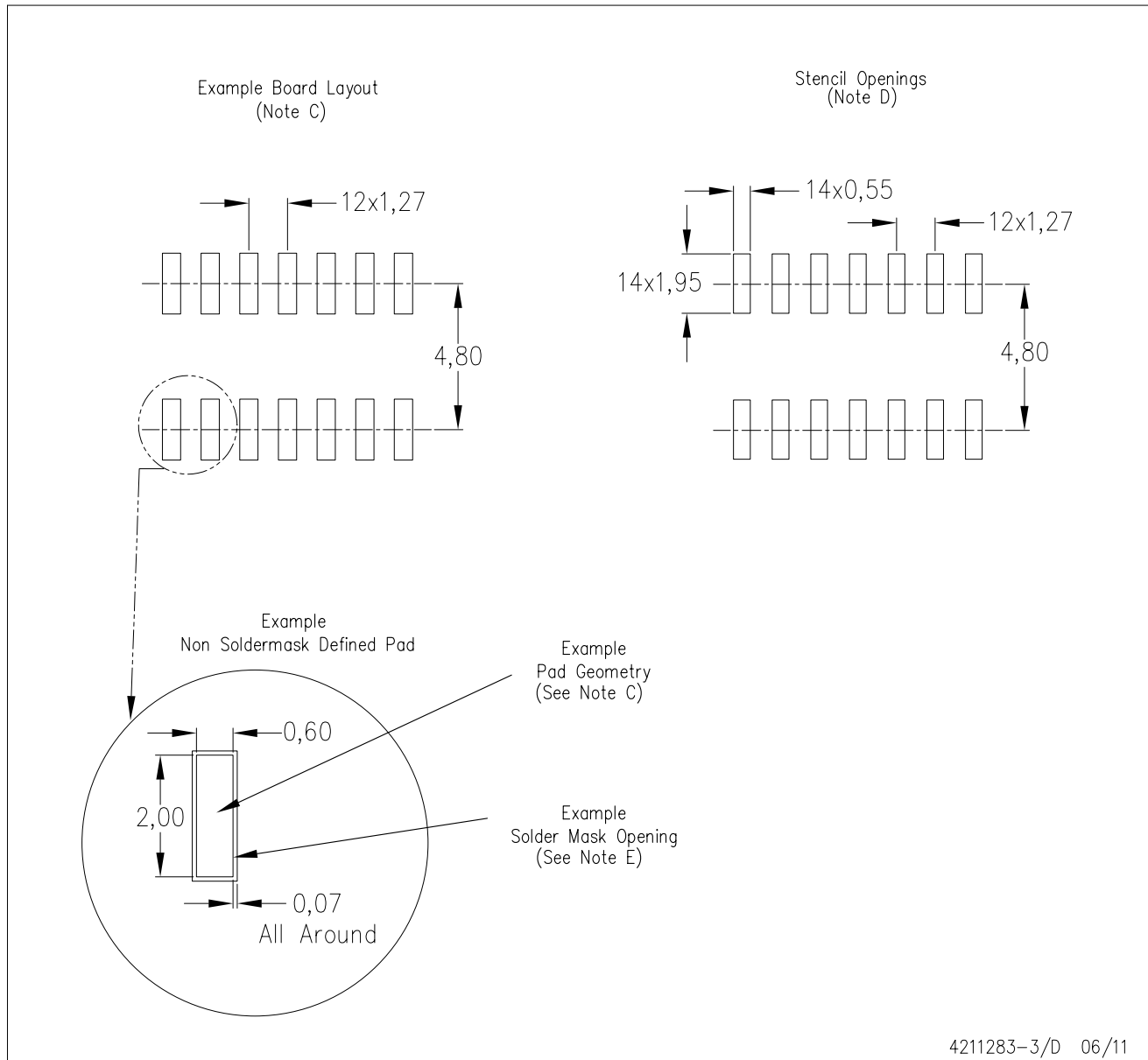


## NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- D. Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AB.

D (R-PDSO-G14)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Publication IPC-7351 is recommended for alternate designs.
  - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  - E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

# MECHANICAL DATA

NS (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

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### Applications

|                               |                                                                                          |
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