

SN54BCT25245, SN74BCT25245 25-Ω OCTAL BUS TRANSCEIVERS WITH 3-STATE OUTPUTS

SCBS053B – MAY 1990 – REVISED APRIL 1994

- State-of-the-Art BiCMOS Design Significantly Reduces I_{CCZ}
- ESD Protection Exceeds 2000 V Per MIL-STD-883C, Method 3015; Exceeds 200 V Using Machine Model (C = 200 pF, R = 0)
- Designed to Facilitate Incident-Wave Switching for Line Impedances of 25 Ω or Greater
- Distributed V_{CC} and GND Pins Minimize Noise Generated by the Simultaneous Switching of Outputs
- Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic and Ceramic 300-mil DIPs (JT, NT)

description

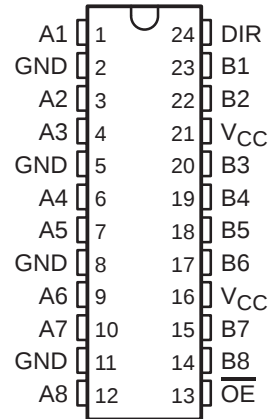
The 'BCT25245 is a 25-Ω octal bus transceiver designed for asynchronous communication between data buses. It improves both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented transceivers.

The device allows data transmission from the A bus to the B bus or from the B bus to the A bus depending upon the logic level at the direction-control (DIR) input. The output-enable ($\overline{OE}$) input can disable the device so that both buses are effectively isolated.

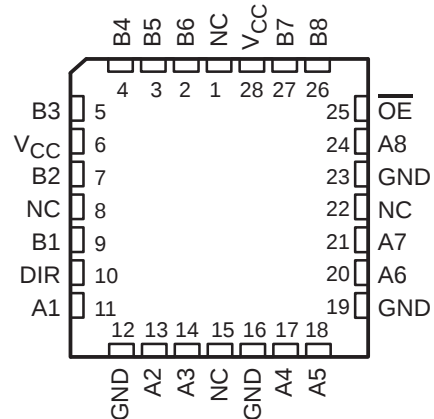
These transceivers are capable of sinking 188-mA I_{OL} , which facilitates switching 25-Ω transmission lines on the incident wave. The distributed V_{CC} and GND pins minimize switching noise for more reliable system operation.

The SN54BCT25245 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74BCT25245 is characterized for operation from 0°C to 70°C.

SN54BCT25245 . . . JT OR W PACKAGE
SN74BCT25245 . . . DW OR NT PACKAGE
(TOP VIEW)



SN54BCT25245 . . . FK PACKAGE
(TOP VIEW)



NC – No internal connection

FUNCTION TABLE

INPUTS		OPERATION
$\overline{OE}$	DIR	
L	L	B data to A bus
L	H	A data to B bus
H	X	Isolation

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The diagram shows the internal logic of the 74VHC147 decoder. It includes two 3-input AND gates at the top, two inverters in the middle, and two 3-input OR gates at the bottom. The inputs are labeled DIR (24), A1 (1), and B1 (23). The output is labeled OE (13). The logic is as follows:

- The top-left AND gate has inputs DIR, A1, and B1. Its output is one of the three lines grouped under the bracket labeled "To Seven Other Channels".
- The top-right AND gate has inputs DIR, A1, and B1. Its output is one of the three lines grouped under the bracket labeled "To Seven Other Channels".
- The middle-left inverter has input A1. Its output is one of the three lines grouped under the bracket labeled "To Seven Other Channels".
- The middle-right inverter has input B1. Its output is one of the three lines grouped under the bracket labeled "To Seven Other Channels".
- The bottom-left OR gate has inputs DIR, A1, and B1. Its output is one of the three lines grouped under the bracket labeled "To Seven Other Channels".
- The bottom-right OR gate has inputs DIR, A1, and B1. Its output is one of the three lines grouped under the bracket labeled "To Seven Other Channels".

Pin numbers shown are for the DW, JT, NT, and W packages.

Supply voltage range, V_{CC}		–0.5 V to 7 V
Input voltage range, V_I (see Note 1): Control inputs		–0.5 V to 7 V
I/O ports		–0.5 V to 5.5 V
Voltage range applied to any output in the disabled or power-off state, V_O		–0.5 V to 5.5 V
Voltage range applied to any output in the high state, V_O (B port)		–0.5 V to V_{CC}
Input clamp current, I_{IK}		–30 mA
Current into any output in the low state, I_O : SN54BCT25245 (A port)		250 mA
SN54BCT25245 (B port)		40 mA
SN74BCT25245 (A port)		376 mA
SN74BCT25245 (B port)		48 mA
Operating free-air temperature range: SN54BCT25245		–55°C to 125°C
SN74BCT25245		0°C to 70°C
Storage temperature range		–65°C to 150°C

NOTE 1: The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

SN54BCT25245, SN74BCT25245

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recommended operating conditions

		SN54BCT25245			SN74BCT25245			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC}	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH}	High-level input voltage	2			2			V
V_{IL}	Low-level input voltage			0.8			0.8	V
I_{IK}	Input clamp current			–18			–18	mA
I_{OH}	High-level output current	A port		–53			–80	mA
		B port		–3			–3	
I_{OL}	Low-level output current	A port		125			188	mA
		B port		20			24	
T_A	Operating free-air temperature	–55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		SN54BCT25245			SN74BCT25245			UNIT
				MIN	TYP†	MAX	MIN	TYP†	MAX	
V_{IK}		$V_{CC} = 4.5\text{ V}$, $I_I = -18\text{ mA}$				–1.2			–1.2	V
V_{OH}	A port	$V_{CC} = 4.5\text{ V}$	$I_{OH} = -53\text{ mA}$	2						V
			$I_{OH} = -80\text{ mA}$				2			
	B port	$V_{CC} = 4.75\text{ V}$, $V_{CC} = 4.5\text{ V}$	$I_{OH} = -3\text{ mA}$				2.7			
V_{OL}	A port	$V_{CC} = 4.5\text{ V}$	$I_{OL} = 94\text{ mA}$	0.38	0.55		0.42	0.55		V
			$I_{OL} = 125\text{ mA}$		0.8					
			$I_{OL} = 188\text{ mA}$					0.7		
	B port	$V_{CC} = 4.5\text{ V}$	$I_{OL} = 20\text{ mA}$	0.3	0.5					
			$I_{OL} = 24\text{ mA}$				0.35	0.5		
I_I	A or B port	$V_{CC} = 5.5\text{ V}$, $V_I = 5.5\text{ V}$			0.25			0.25		mA
	Control input				0.1			0.1		
$I_{IH}^{\ddagger}$	A or B port	$V_{CC} = 5.5\text{ V}$, $V_I = 2.7\text{ V}$			70			70		μA
	Control input				20			20		
$I_{IL}^{\ddagger}$	A or B port	$V_{CC} = 5.5\text{ V}$, $V_I = 0.5\text{ V}$			–0.6			–0.6		mA
	Control input				–0.6			–0.6		
$I_{OS}^{\S}$	B port only [¶]	$V_{CC} = 5.5\text{ V}$, $V_O = 0$		–60		–150	–60		–150	mA
I_{CCH}	A to B	$V_{CC} = 5.5\text{ V}$		36	46		36	46		mA
	B to A			63	80		63	80		
I_{CCL}	A to B	$V_{CC} = 5.5\text{ V}$		48	60		48	60		mA
	B to A			95	125		95	125		
I_{CCZ}		$V_{CC} = 5.5\text{ V}$		12	16		12	16		mA
C_i	Control input	$V_{CC} = 5\text{ V}$, $V_I = 2.5\text{ V}$ or 0.5 V		8			8			pF
C_{io}	A port	$V_{CC} = 5\text{ V}$, $V_O = 2.5\text{ V}$ or 0.5 V		18			18			pF
	B port			8			8			

† All typical values are at $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$.

‡ For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.

§ Not more than one output should be tested at a time, and the duration of the test should not exceed one second.

¶ Testing for this parameter on the A port is not recommended.

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switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 5 V, C _L = 50 pF, R1 = 500 Ω, R2 = 500 Ω, T _A = 25°C			V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R1 = 500 Ω, R2 = 500 Ω, T _A = MIN to MAX†				UNIT
			‘BCT25245			SN54BCT25245		SN74BCT25245		
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	A	B	1.2	3.3	5.1	1.2	5.8	1.2	5.7	ns
t _{PHL}			1.9	4.3	6.7	1.9	7.6	1.9	7.2	
t _{PLH}	B	A	1.2	3.3	4.8	1.2	5.7	1.2	5.5	ns
t _{PHL}			2.1	4	5.6	2.1	6.4	2.1	6.2	
t _{PZH}	OE	A	3.7	6.3	8.4	3.7	10.1	3.7	9.6	ns
t _{PZL}			4.5	7.4	9.2	4.5	11.1	4.5	10.3	
t _{PHZ}	OE	A	1.8	3.7	5.5	1.8	6.4	1.8	6.2	ns
t _{PLZ}			3.3	5.1	7.2	3.3	9.6	3.3	8.3	
t _{PZH}	OE	B	3.4	5.7	7.9	3.4	9.2	3.4	8.9	ns
t _{PZL}			4.3	6.6	8.7	4.3	10.1	4.3	9.7	
t _{PHZ}	OE	B	2.7	4.5	6.3	2.7	7.2	2.7	6.9	ns
t _{PLZ}			1.7	4.5	6.8	1.7	8.3	1.7	7.5	

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

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